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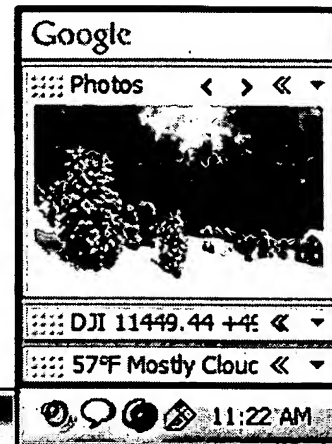
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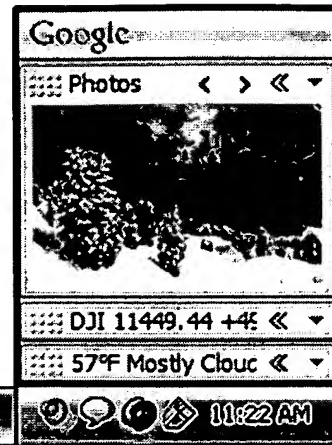
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Lee-Ren Ton; Lung-Chung Chang; Min-Fu Kao; Han-Min Tseng; Shi-Sheng Si Ma; Dze-Chaung Wang; Chung-Ping Chung;
[Parallel and Distributed Systems, 1997. Proceedings., 1997 International Conf](#)
10-13 Dec. 1997 Page(s):138 - 143
Digital Object Identifier 10.1109/ICPADS.1997.652541
[AbstractPlus](#) | Full Text: [PDF\(600 KB\)](#) IEEE CNF
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- ☐ **16. Hardware realization of a Java virtual machine for high performance multi applications**
Berekovic, M.; Kloos, H.; Pirsch, P.;
[Signal Processing Systems, 1997. SIPS 97 - Design and Implementation., 1997 Workshop on](#)
3-5 Nov. 1997 Page(s):479 - 488
Digital Object Identifier 10.1109/SIPS.1997.626325
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- ☐ 1. **Implementation of pipelined data encryption standard (DES) using Altera**
 Teo Pock Chueng; Yusoff, Z.M.; Sha'ameri, A.Z.;
[TENCON 2000. Proceedings](#)
 Volume 3, 24-27 Sept. 2000 Page(s):17 - 21 vol.3
 Digital Object Identifier 10.1109/TENCON.2000.892211
[AbstractPlus](#) | Full Text: [PDF](#)(276 KB) IEEE CNF
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 Sentieys, O.; Diguët, J.Ph.; Philippe, J.L.; Martin, E.;
[ASIC Conference and Exhibit, 1996. Proceedings., Ninth Annual IEEE Internat](#)
 23-27 Sept. 1996 Page(s):147 - 150
 Digital Object Identifier 10.1109/ASIC.1996.551981
[AbstractPlus](#) | Full Text: [PDF](#)(388 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **ATCURE: a heterogeneous processor for image recognition**
 Ackenhusen, J.G.; Holmes, Q.A.; Kortesoja, P.A.; McCubbrey, D.L.; Mohan, P.
 Wessling, T.N.; Witter, L.J.; Stopper, H.;
[Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 Internatio](#)
[on](#)
 Volume 4, 9-12 May 1995 Page(s):2679 - 2682 vol.4
 Digital Object Identifier 10.1109/ICASSP.1995.480113
[AbstractPlus](#) | Full Text: [PDF](#)(588 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Data flow processor chip for image processing**
 Temma, T.; Iwashita, M.; Matsumoto, K.; Kurokawa, H.; Nukiyama, T.;
[Electron Devices, IEEE Transactions on](#)
 Volume 32, Issue 9, Sep 1985 Page(s):1784 - 1791
[AbstractPlus](#) | Full Text: [PDF](#)(1224 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **VLSI for high-performance graphic control utilizing multiprocessor archit**
 Katsura, K.; Maejima, H.; Minorikawa, K.; Yonezawa, H.;
[Electron Devices, IEEE Transactions on](#)
 Volume 32, Issue 11, Nov 1985 Page(s):2232 - 2237

[AbstractPlus](#) | Full Text: [PDF\(752 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **6. Video encoder architecture for MPEG2 real time encoding**
Geng-Lin Chen; Jyh-Shin Pan; Jia-Lung Wang;
[Consumer Electronics, IEEE Transactions on](#)
Volume 42, Issue 3, Aug. 1996 Page(s):290 - 299
Digital Object Identifier 10.1109/30.536123

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(628 KB\)](#) IEEE JNL
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- ☐ **7. A pipelined low power architectural MPEG-4 video codec chip with deblo**
mobile wireless multimedia applications
Bontae Koo; Seongmin Kim; Seokho Lee; Minseok Choi; Kihyuk Park; Nakwoc
Jongdae Kim; Hyunmook Cho;
[ASIC, 2003. Proceedings. 5th International Conference on](#)
Volume 2, 21-24 Oct. 2003 Page(s):934 - 937 Vol.2
Digital Object Identifier 10.1109/ICASIC.2003.1277364

[AbstractPlus](#) | Full Text: [PDF\(428 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **8. ArchMDSP: using DSPs for parallel image processing**
Mucheroni, M.L.; Moron, C.E.; Saito, J.H.;
[EUROMICRO 97. 'New Frontiers of Information Technology', Proceedings of t](#)
[EUROMICRO Conference](#)
1-4 Sept. 1997 Page(s):584 - 590
Digital Object Identifier 10.1109/EURMIC.1997.617377

[AbstractPlus](#) | Full Text: [PDF\(404 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **9. Module generation based VLSI implementation of a demultiplexer for satc**
communications
Jamali, M.M.; Kwatra, S.C.; Shetty, D.H.;
[Circuits and Systems, 1996. ISCAS '96. 'Connecting the World', 1996 IEEE Ir](#)
[Symposium on](#)
Volume 4, 12-15 May 1996 Page(s):364 - 367 vol.4
Digital Object Identifier 10.1109/ISCAS.1996.541977

[AbstractPlus](#) | Full Text: [PDF\(252 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **10. Silicon assembler design of a DCT/IDCT ASIC for real-time JPEG/MPEG c**
Kocher, M.; Rose, K.;
[ASIC Conference and Exhibit, 1992., Proceedings of Fifth Annual IEEE Intern](#)
21-25 Sept. 1992 Page(s):185 - 188
Digital Object Identifier 10.1109/ASIC.1992.270281

[AbstractPlus](#) | Full Text: [PDF\(192 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **11. Throughput analysis of pipelined multiprocessor modules**
Lee, S.-Y.;
[Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on the](#)
19-21 Oct. 1992 Page(s):548 - 550
Digital Object Identifier 10.1109/FMPC.1992.234926

[AbstractPlus](#) | Full Text: [PDF\(228 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **12. A 1.5 Gb/s 8x8 cross-connect switch using a time reservation algorithm**
Matsunaga, H.; Uematsu, H.;
[Selected Areas in Communications, IEEE Journal on](#)

Volume 9, Issue 8, Oct. 1991 Page(s):1308 - 1317
Digital Object Identifier 10.1109/49.105177

[AbstractPlus](#) | [Full Text: PDF\(796 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **13. Design of a 32 b monolithic microprocessor based on GaAs HEMSFET technology**
Tien, C.-K.V.; Lewis, K.; Greub, H.J.; Tsen, T.; McDonald, J.F.;
[Very Large Scale Integration \(VLSI\) Systems, IEEE Transactions on](#)
Volume 5, Issue 2, June 1997 Page(s):238 - 243
Digital Object Identifier 10.1109/92.585228
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(152 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **14. Efficient architectures for computations over variable dimensional Galois fields**
Hasan, M.A.; Ebtedaei, M.;
[Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on](#)
[Circuits and Systems I: Regular Papers, IEEE Transactions on](#)
Volume 45, Issue 11, Nov. 1998 Page(s):1205 - 1211
Digital Object Identifier 10.1109/81.735445
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(252 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **15. Real-time DSP implementation for MRF-based video motion detection**
Dumontier, C.; Luthon, F.; Charras, J.-P.;
[Image Processing, IEEE Transactions on](#)
Volume 8, Issue 10, Oct. 1999 Page(s):1341 - 1347
Digital Object Identifier 10.1109/83.791960
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(652 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **16. FFT Processor IP Cores synthesis on the base of configurable pipeline architecture**
Melnyk, A.; Dunets, B.;
[CAD Systems in Microelectronics, 2003. CADSM 2003. Proceedings of the 7th](#)
[Conference. The Experience of Designing and Application of](#)
18-22 Feb. 2003 Page(s):211 - 213
[AbstractPlus](#) | [Full Text: PDF\(240 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **17. A modular pipelined implementation of large fast Fourier transforms**
El-Khashab, A.M.; Swartzlander, E.E., Jr.;
[Signals, Systems and Computers, 2002. Conference Record of the Thirty-Sixth](#)
[Conference on](#)
Volume 2, 3-6 Nov. 2002 Page(s):995 - 999 vol.2
[AbstractPlus](#) | [Full Text: PDF\(389 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **18. A high-throughput pipelined architecture for blind adaptive equalizer with low latency**
Mizuno, M.; Ueda, K.; Okello, J.; Ochi, H.;
[Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on](#)
Volume 2, 4-7 Aug. 2002 Page(s):II - 29-32 vol.2
[AbstractPlus](#) | [Full Text: PDF\(297 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **19. A single-chip real-time programmable video signal processor**
Lingfeng Li; Danian Gong; Yun He;
[Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on](#)
Volume 5, 26-29 May 2002 Page(s):V-129 - V-132 vol.5
Digital Object Identifier 10.1109/ISCAS.2002.1010657

[AbstractPlus](#) | Full Text: [PDF\(428 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **20. A hardware-driven profiling scheme for identifying program hot spots to optimization**
 Merten, M.C.; Trick, A.R.; George, C.N.; Gyllenhaal, J.C.; Hwu, W.W.;
[Computer Architecture, 1999. Proceedings of the 26th International Symposium](#)
 2-4 May 1999 Page(s):136 - 148
 Digital Object Identifier 10.1109/ISCA.1999.765946
[AbstractPlus](#) | Full Text: [PDF\(304 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **21. Hybrid testing schemes based on mutual and signature testing**
 Abdulla, M.F.; Ravikumar, C.P.; Kumar, A.;
[VLSI Design, 1998. Proceedings., 1998 Eleventh International Conference on](#)
 4-7 Jan. 1998 Page(s):293 - 296
 Digital Object Identifier 10.1109/ICVD.1998.646621
[AbstractPlus](#) | Full Text: [PDF\(480 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **22. Interfacing synchronous and asynchronous modules within a high-speed**
 Sjogren, A.E.; Myers, C.J.;
[Advanced Research in VLSI, 1997. Proceedings., Seventeenth Conference on](#)
 15-16 Sept. 1997 Page(s):47 - 61
 Digital Object Identifier 10.1109/ARVLSI.1997.634845
[AbstractPlus](#) | Full Text: [PDF\(808 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **23. Compact and flexible linear-array-based implementations of a pipeline of modules (PMMLA) for high throughput applications**
 Soo-Young Lee; Ghare, G.;
[High Performance Computing, 1997. Proceedings. Fourth International Confer](#)
 18-21 Dec. 1997 Page(s):296 - 301
 Digital Object Identifier 10.1109/HIPC.1997.634506
[AbstractPlus](#) | Full Text: [PDF\(640 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **24. The design and implementation of FDDI microcomputer network system**
 LiMin Sun; WenHua Dou; DongXia Wang; Gang Xiao;
[Communication Technology Proceedings, 1996. ICCT'96., 1996 International](#)
 Volume 2, 5-7 May 1996 Page(s):619 - 622 vol.2
 Digital Object Identifier 10.1109/ICCT.1996.544959
[AbstractPlus](#) | Full Text: [PDF\(276 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **25. Systems oriented architecture of image processor for automated visual i**
 Hattori, T.; Yamasaki, T.; Nakada, M.;
[Systems, Man and Cybernetics, 1995. 'Intelligent Systems for the 21st Century](#)
[International Conference on](#)
 Volume 4, 22-25 Oct. 1995 Page(s):3093 - 3098 vol.4
 Digital Object Identifier 10.1109/ICSMC.1995.538257
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- ☐ 1. **JGram: rapid development of multi-agent pipelines for real-world tasks**
Sukthankar, R.; Brusseau, A.; Pelletier, R.; Stockton, R.;
[Agent Systems and Applications, 1999 and Third International Symposium on Proceedings. First International Symposium on](#)
3-6 Oct. 1999 Page(s):30 - 40
Digital Object Identifier 10.1109/ASAMA.1999.805391
[AbstractPlus](#) | Full Text: [PDF](#)(100 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **A dual issue queued pipelined Java processor TRAJA-toward an open so project**
Shimizu, N.; Naito, M.;
[ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on](#)
23-25 Aug. 1999 Page(s):213 - 216
Digital Object Identifier 10.1109/APASIC.1999.824066
[AbstractPlus](#) | Full Text: [PDF](#)(264 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **The JAFARDD processor: a Java architecture based on a Folding Algorithm Reservation stations, Dynamic translation, and Dual processing**
El-Kharashi, M.W.; Gebali, F.; Li, K.F.; Fang Zhang;
[Consumer Electronics, IEEE Transactions on](#)
Volume 48, Issue 4, Nov 2002 Page(s):1004 - 1015
Digital Object Identifier 10.1109/TCE.2003.1196432
[AbstractPlus](#) | Full Text: [PDF](#)(848 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **Analysis Prediction Template Toolkit (APTT) for object-based computation**
Fleury, M.; Downton, A.C.; Clark, A.F.;
[Software, IEE Proceedings- \[see also Software Engineering, IEE Proceedings\]](#)
Volume 147, Issue 2, April 2000 Page(s):37 - 47
Digital Object Identifier 10.1049/ip-sen:20000572
[AbstractPlus](#) | Full Text: [PDF](#)(1304 KB) IEE JNL
- ☐ 5. **Compiler supported coarse-grained pipelined parallelism: why and how**
Wei Du; Agrawal, G.;
[Parallel and Distributed Processing Symposium, 2003. Proceedings. Internatio](#)
22-26 April 2003 Page(s):8 pp.

Digital Object Identifier 10.1109/IPDPS.2003.1213373

[AbstractPlus](#) | Full Text: [PDF\(354 KB\)](#) IEEE CNF
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- ☐ **6. Modeling and evaluation of control flow prediction schemes using compilation simulation and Java workloads**
Tao Li; John, L.K.; Bell, R.H., Jr.;
[Modeling, Analysis and Simulation of Computer and Telecommunications Systems](#)
[MASCOTS 2002. Proceedings. 10th IEEE International Symposium on](#)
11-16 Oct. 2002 Page(s):391 - 400
Digital Object Identifier 10.1109/MASCOT.2002.1167100
[AbstractPlus](#) | Full Text: [PDF\(541 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **7. A real-time Java system on a multithreaded Java microcontroller**
Pfeffer, M.; Uhrig, S.; Ungerer, T.; Brinkschulte, U.;
[Object-Oriented Real-Time Distributed Computing. 2002. \(ISORC 2002\). Proceedings](#)
[IEEE International Symposium on](#)
29 April-1 May 2002 Page(s):34 - 41
Digital Object Identifier 10.1109/ISORC.2002.1003658
[AbstractPlus](#) | Full Text: [PDF\(279 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **8. The optimum pipeline depth for a microprocessor**
Hartstein, A.; Puzak, T.R.;
[Computer Architecture. 2002. Proceedings. 29th Annual International Symposium on](#)
25-29 May 2002 Page(s):7 - 13
Digital Object Identifier 10.1109/ISCA.2002.1003557
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **9. Modelling and characterization of pipelined ADCs**
Dallet, D.; Daponte, P.; Mancini, E.; Rapuano, S.;
[Instrumentation and Measurement Technology Conference. 2002. IMTC/2002. the 19th IEEE](#)
Volume 1, 21-23 May 2002 Page(s):207 - 211 vol.1
Digital Object Identifier 10.1109/IMTC.2002.1006841
[AbstractPlus](#) | Full Text: [PDF\(768 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **10. An application specific Java processor with reconfigurabilities**
Kimura, S.; Kida, H.; Takagi, K.; Abernatsu, T.; Watanabe, K.;
[Design Automation Conference. 2000. Proceedings of the ASP-DAC 2000. As](#)
[Pacific](#)
25-28 Jan. 2000 Page(s):25 - 26
Digital Object Identifier 10.1109/ASPDAC.2000.835061
[AbstractPlus](#) | Full Text: [PDF\(260 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **11. Overcoming the limitations of traditional media for teaching modern processing**
Marwedel, P.; Sirocic, B.;
[Microelectronic Systems Education. 2003. Proceedings. 2003 IEEE International](#)
1-2 June 2003 Page(s):102 - 103
[AbstractPlus](#) | Full Text: [PDF\(359 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **12. SPaDES/Java: object-oriented parallel discrete-event simulation**
Yong Meng Teo; Yew Kwong Ng;
[Simulation Symposium. 2002. Proceedings. 35th Annual](#)

14-18 April 2002 Page(s):245 - 252

[AbstractPlus](#) | [Full Text: PDF\(273 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **13. Issues in the design of store buffers in dynamically scheduled processor**
Bhargava, R.; John, L.K.;
[Performance Analysis of Systems and Software, 2000. ISPASS. 2000 IEEE In Symposium on](#)
24-25 April 2000 Page(s):76 - 87
Digital Object Identifier 10.1109/ISPASS.2000.842285
[AbstractPlus](#) | [Full Text: PDF\(512 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **14. Delft-Java dynamic translation**
Glossner, J.; Vassiliadis, S.;
[EUROMICRO Conference, 1999. Proceedings. 25th](#)
Volume 1, 8-10 Sept. 1999 Page(s):57 - 62 vol.1
Digital Object Identifier 10.1109/EURMIC.1999.794446
[AbstractPlus](#) | [Full Text: PDF\(60 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **15. Non-stalling counterflow architecture**
Miller, M.F.; Janik, K.J.; Shih-Lien Lu;
[High-Performance Computer Architecture, 1998. Proceedings., 1998 Fourth In Symposium on](#)
1-4 Feb. 1998 Page(s):334 - 341
Digital Object Identifier 10.1109/HPCA.1998.650572
[AbstractPlus](#) | [Full Text: PDF\(88 KB\)](#) [IEEE CNF](#)
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- ☐ **16. Instruction folding in Java processor**
Lee-Ren Ton; Lung-Chung Chang; Min-Fu Kao; Han-Min Tseng; Shi-Sheng Si Ma; Dze-Chaung Wang; Chung-Ping Chung;
[Parallel and Distributed Systems, 1997. Proceedings., 1997 International Conf](#)
10-13 Dec. 1997 Page(s):138 - 143
Digital Object Identifier 10.1109/ICPADS.1997.652541
[AbstractPlus](#) | [Full Text: PDF\(600 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **17. Autonomous system for web-based microarray image analysis**
Bozinov, D.;
[NanoBioscience, IEEE Transactions on](#)
Volume 2, Issue 4, Dec. 2003 Page(s):215 - 220
Digital Object Identifier 10.1109/TNB.2003.817021
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(576 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

- ☐ **18. MPI and Java-MPI: Contrasts and Comparisons of Low-Level Communication Performance**
Getov, V.; Gray, P.; Sunderam, V.;
[Supercomputing, ACM/IEEE 1999 Conference](#)
13-18 Nov. 1999 Page(s):21 - 21
Digital Object Identifier 10.1109/SC.1999.10005
[AbstractPlus](#) | [Full Text: PDF\(168 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **19. A design space analysis of Java processors**
El-Kharashi, M.W.; Pfrimmer, J.; Li, K.F.; Gebali, F.;
[Communications, Computers and signal Processing, 2003. PACRIM. 2003 IEEE](#)

[Conference on](#)

Volume 1, 28-30 Aug. 2003 Page(s):159 - 163 vol.1

[AbstractPlus](#) | Full Text: [PDF\(420 KB\)](#) IEEE CNF[Rights and Permissions](#)

- ☐ **20. Object oriented metrics: precision tools and configurable visualisations**
Irwin, W.; Churcher, N.;
[Software Metrics Symposium, 2003. Proceedings. Ninth International](#)
3-5 Sept. 2003 Page(s):112 - 123
[AbstractPlus](#) | Full Text: [PDF\(536 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **21. Architecture design, system implementation, and applications of MPEG-4**
Yi-Shin Tung; Ja-Ling Wu;
[MPEG-4, 2001 Proceedings of Workshop and Exhibition on](#)
18-20 June 2001 Page(s):37 - 40
Digital Object Identifier 10.1109/MPEG.2001.996442
[AbstractPlus](#) | Full Text: [PDF\(594 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **22. Low-level analysis of a portable Java byte code WCET analysis framework**
Bate, I.; Bernat, G.; Murphy, G.; Puschner, P.;
[Real-Time Computing Systems and Applications, 2000. Proceedings. Seventh Conference on](#)
12-14 Dec. 2000 Page(s):39 - 46
Digital Object Identifier 10.1109/RTCSA.2000.896369
[AbstractPlus](#) | Full Text: [PDF\(648 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **23. PTEP: the parallel telemetry processor**
Norris, J.S.; Backes, P.G.; Baumgartner, E.T.;
[Aerospace Conference, 2001, IEEE Proceedings.](#)
Volume 7, 10-17 March 2001 Page(s):7 - 3345 vol.7
Digital Object Identifier 10.1109/AERO.2001.931410
[AbstractPlus](#) | Full Text: [PDF\(1088 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **24. A Java processor architecture with bytecode folding and dynamic scheduling**
El-Kharashi, M.W.; Elguibaly, F.; Li, K.F.;
[Communications, Computers and signal Processing, 2001. PACRIM. 2001 IEEE Conference on](#)
Volume 1, 26-28 Aug. 2001 Page(s):307 - 310 vol.1
Digital Object Identifier 10.1109/PACRIM.2001.953584
[AbstractPlus](#) | Full Text: [PDF\(432 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **25. A Java processor suitable for applications of smart card**
Zhang Jianjie; Li Feihui; Ge Yuanqing; Yue Zhenwu; Yang Zhilian;
[ASIC, 2001. Proceedings, 4th International Conference on](#)
23-25 Oct. 2001 Page(s):736 - 739
Digital Object Identifier 10.1109/ICASIC.2001.982668
[AbstractPlus](#) | Full Text: [PDF\(318 KB\)](#) IEEE CNF
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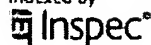
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 Duong Trung;
Petroleum and Chemical Industry Conference, 1995. Record of Conference Proceedings. Applications Society 42nd Annual
 11-13 Sept. 1995 Page(s):299 - 305
 Digital Object Identifier 10.1109/PCICON.1995.523965
[AbstractPlus](#) | Full Text: [PDF](#)(700 KB) IEEE CNF
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- ☐ 2. **Parameterized and low power DSP core for embedded systems**
 Ya-Lan Tsao; Ming Hsuan Tan; Jun-Xian Teng; Shyh-Jye Jou;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
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- ☐ 3. **V-SAT: a visual specification and analysis tool for system-on-chip explor**
 Khare, A.; Savoiu, N.; Halambi, A.; Grun, P.; Dutt, N.; Nicolau, A.;
EUROMICRO Conference, 1999. Proceedings. 25th
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- #18 (((pipeline<in>metadata) <and> (graphical<in>metadata)) <and> (interface<in>metadata)) <and> (pyr >= 1950 <and> pyr <= 2003)



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	L94	L93 and (pipeline near5 data)	93

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END OF SEARCH HISTORY